

An industrial application of formal model based development: the Metrô Rio ATP case

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Outline

- 1 Railway Signaling Context and ATP
- 2 Modelling Guidelines
- 3 Architecture Definition
- 4 Conclusion

Railway Signaling Context: GETS

Safety Critical Systems such as interlocking system and automatic train protection system.

General Electric Transportation System (GETS) :

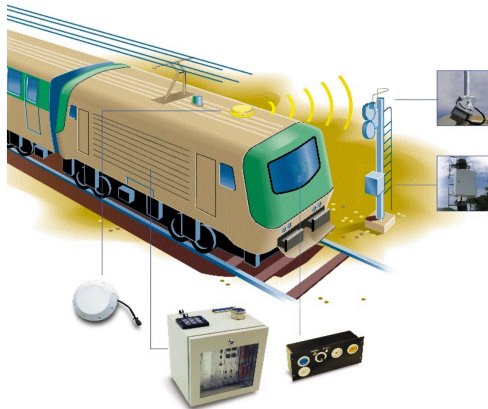
- Effort to adopt formal methods for own development process
- Collaboration with the Faculty of Engineering of the University of Florence
- Experimentation of different technologies and tools for formal verification

Railway Signaling: ATP

- ATP systems
 - Focus on on-board equipment
 - Control modes logic
 - Message analysis algorithms
- CENELEC EN 50128 based development
 - European norm for railway
 - SIL-4 products (Higher level of integrity)
 - What about generated code?
- Development choices
 - Stateflow
 - Real Time Workshop Embedded Coder
 - MAAB Guidelines

Railway Signaling: ATP

Automatic Train Protection (ATP) systems



Development Choices

Challenges:

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- **MAAB Guidelines**: Focused on the design of automotive systems, not for signaling systems
- **Stateflow**: Has not a rigorous formal semantics
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- How did we address these shortcomings?

MAAB Guidelines

Adaptation of existing Guidelines through priority restrictions:

Title	Priority	Restriction
Transitions in Flowcharts	HR	M
Use of return value from graphical functions	R	SR
Bitwise Stateflow operators	SR	R
Use of unary minus on unsigned integers in Stateflow	R	M
Reuse of variables within a single Stateflow scope	R	M
Comparison operation in Stateflow	R	M
Scope of internal signals and local auxiliary variables	SR	M
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Table: MAAB Guidelines adaptation

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Additional Guidelines

Some other guidelines are needed for code generation issue:

- **Events shall not be used in Stateflow diagrams:** to avoid recursive call and then to save from infinite recursion and stack overflow
- **States and junctions shall not be used jointly :** to exclude backtracking without undo
- **Outgoing transitions shall have mutually exclusive conditions on their guards:** to avoid incorrect determinism on transitions evaluated in clockwise rule

Architecture Definition Approach

- Architecture definition must take into account of automatic code generation.

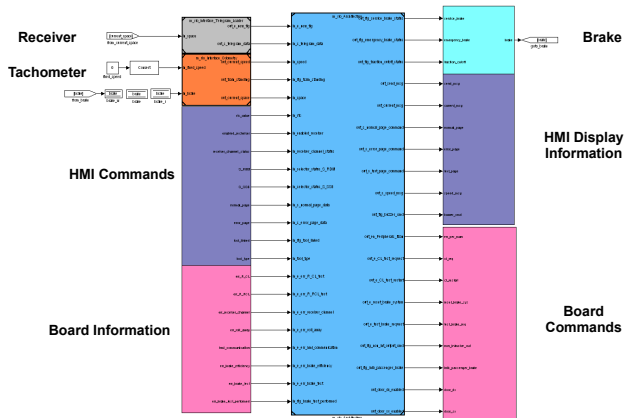
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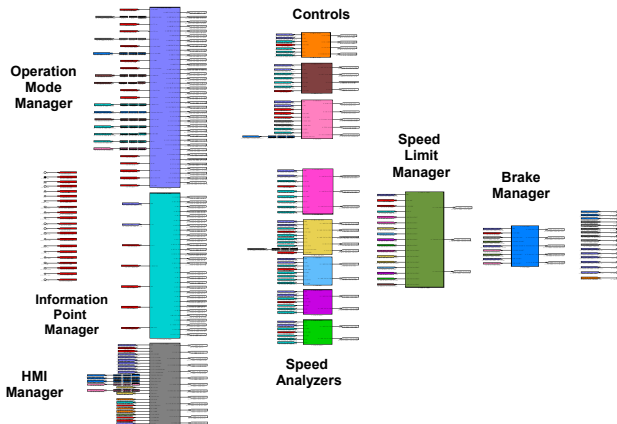
Architecture Definition Approach

- Architecture definition must take into account of automatic code generation.
- Multi-level architecture approach was defined to derive a formal model for the system
- The Goal is to create formal models that makes sense in terms of the architecture of the software system

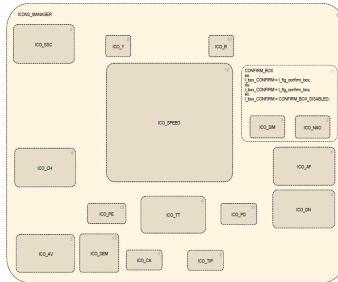
Context level



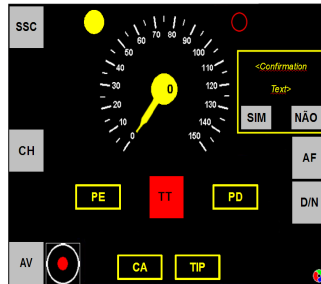
Architecture level



Detail level



HMI Manager
Design



HMI

Unit Requirements Decomposition

Req: When the rain function button (CH) is pressed and released within trainfunction milliseconds and the train is standing, the icon of the rain function shall be lighted on and the target speed when approaching the platform shall be reduced to 40 km/h if the train is positioned outdoor.

- 1 If the rain function button (CH) is pressed and released within trainfunction milliseconds the rain event shall be raised: HMI Manager
- 2 If the rain event is raised and the train is standing the rain function shall be activated: Platform Control
- 3 If the rain function is active and the train is positioned outdoor the target speed shall be reduced to 40km/h: Platform Speed Analyser
- 4 If the rain function is active the icon of the rain function button shall be lighted on: HMI Manager

Results

- Definition of 438 unit requirements that led to 13 statecharts
- 14 source generated files, one for each chart and one to manage the integration of the other units
- Approximately 120K lines of code

Project	#Modules	LOC	#Bugs	Man/H
SSC Metro Rio	13	120K	33	16
SSC BL1Plus	12	40K	114	105

Table: Bug detection and correction costs for comparable project which required a modeling cost of approximately 4 man/months

Conclusion

- Notable reduction of bugs
- The novel design approach permitted to strongly reduce bugs detection time
- Design time increases approximately of 30%
- Verification time with model based testing and abstract interpretation for generated code reduced by 70%